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A diagram of a 5-bit shift register. It consists of five rectangular cells connected in a horizontal line. Each cell has an input on the left and an output on the right. The inputs are labeled A, B, C, D, and E from left to right. The outputs are also labeled A, B, C, D, and E from left to right. The output of the first cell (A) is connected to the input of the second cell (B), and so on, forming a chain.

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