

2015 - 2016

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A diagram of a 5-bit shift register. It consists of five rectangular cells connected in a horizontal line. The top input of the first cell is labeled 'A', the top input of the second is 'B', the top input of the third is 'C', the top input of the fourth is 'D', and the top input of the fifth is 'E'. The bottom output of the first cell is labeled 'A', the bottom output of the second is 'B', the bottom output of the third is 'C', the bottom output of the fourth is 'D', and the bottom output of the fifth is 'E'. Arrows indicate the flow of data from left to right between the cells.

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- [illegible]

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- <https://www.reumatologhedonne.it/bGtuU10A5j>

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